

ABSTRACT OF THE DISCLOSURE

Disclosed is a bias circuit having a start-up circuit. The bias circuit having a start-up circuit has a bias circuit part using a current mirror circuit, and for generating a constant bias voltage to an output node from an application of a power source voltage, and a start-up circuit part having a capacitor connected between the output node and a common node of in common connecting gates of MOS transistors constructing the current mirror circuit. Accordingly, the bias circuit prevents noise delivered from a power source voltage and power consumption due to static currents, and eliminates the oscillation possibility with stability improved in a high frequency range.